Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11	(doron near orenstien).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L2	10	(Marcelo near Yuffe).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L3	52	(high adj power) near bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L4	129	(low adj power) near bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L5	5940	snoop\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L6	2	(((high adj power) near bus) and ((low adj power) near bus)) and snoop\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L7	14	((high adj power) near bus) and ((low adj power) near bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L8	15473	(first or primary) near bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L9	16011	second\$3 near bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34

L10	8395	((first or primary) near bus) same (second\$3 near bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L11	75	(((first or primary) near bus) same (second\$3 near bus)) same snoop\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L12	133328	low adj power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:34
L13	6	((((first or primary) near bus) same (second\$3 near bus)) same snoop\$4) and (low adj power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:36
L14	894	dual adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:38
L15	3	13 and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:38
L16	16	1 or 2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:39
L17	0	15 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/02 15:39



US Patent & Trademark Office

Subscribe (Full Service) Register (Limited Service, Free) Login

Search: • The ACM Digital Library • The Guide

+dual +bus, +power +saving, +snoop, +high +power +bus, +

THE ACM DIGITAL LIBRARY

Feedback Report a problem Satisfaction

Terms used Found 18 of dual bus power saving snoop high power bus low power bus snoop snooped snooping 150,138

Sort results bγ

Display

results

relevance condensed form

Save results to a Binder Search Tips

Open results in a new

window

Try an Advanced Search Try this search in The ACM Guide

Results 1 - 18 of 18

Relevance scale

1 Poster Session 3: TLB and snoop energy-reduction using virtual caches in low-power chip-multiprocessors

Magnus Ekman, Per Stenström, Fredrik Dahlgren

August 2002 Proceedings of the 2002 international symposium on Low power electronics and design

Full text available: pdf(84.59 KB)

Additional Information: full citation, abstract, references, citings, index terms

Power optimizations for cache memory: Location cache: a low-power L2 cache system Rui Min, Wen-Ben Jone, Yiming Hu



3334334

August 2004 Proceedings of the 2004 international symposium on Low power electronics and design

Full text available: pdf(406.29 KB) Additional Information: full citation, abstract, references, index terms

Design and test of the PowerPC 603 microprocessor

E. K. Vida-Torku, C. H. Malley, Sung Park, R. Reed

March 1995 Proceedings of the 1995 European conference on Design and Test

Full text available: pdf(735.99 KB) Publisher Site

Additional Information: full citation, abstract

Coupling compiler-enabled and conventional memory accessing for energy efficiency Raksit Ashok, Saurabh Chheda, Csaba Andras Moritz

May 2004 ACM Transactions on Computer Systems (TOCS), Volume 22 Issue 2

Full text available: pdf(1.41 MB)

Additional Information: full citation, abstract, references, index terms

Efficient dynamic scheduling through tag elimination

Dan Ernst, Todd Austin

May 2002 ACM SIGARCH Computer Architecture News, Volume 30 Issue 2

Full text available: pdf(1.13 MB) Publisher Site

Additional Information: full citation, abstract, references, citings, index

Platforms: Bluetooth and sensor networks: a reality check

Martin Leopold, Mads Bondo Dydensborg, Philippe Bonnet

November 2003 Proceedings of the 1st international conference on Embedded networked sensor systems

	Full text available: pdf(356.11 KB) Additional Information: full citation, abstract, references, citings, index terms					
7	Formal verification in hardware design: a survey Christoph Kern, Mark R. Greenstreet April 1999 ACM Transactions on Design Automation of Electronic Systems (TODAES),					
	Volume 4 Issue 2 Full text available: pdf(411.53 KB) Additional Information: full citation, abstract, references, citings, index terms					
8	Poster Session 1: Tuning data replication for improving behavior of MPSoC applications O. Ozturk, M. Kandemir, M. J. Irwin, I. Kolcu					
	April 2004 Proceedings of the 14th ACM Great Lakes symposium on VLSI Full text available: pdf(156.21 KB) Additional Information: full citation, abstract, references, index terms					
9	Compiler transformations for high-performance computing David F. Bacon, Susan L. Graham, Oliver J. Sharp December 1994 ACM Computing Surveys (CSUR), Volume 26 Issue 4					
	Full text available: pdf(6.32 MB) Additional Information: full citation, abstract, references, citings, index terms, review					
10	Xunet 2: lessons from an early wide-area ATM testbed Charles R. Kalmanek, Srinivasan Keshav, William T. Marshall, Samuel P. Morgan, Robert C. Restrick February 1997 IEEE/ACM Transactions on Networking (TON), Volume 5 Issue 1					
	Full text available: pdf(231.69 KB) Additional Information: full citation, references, index terms					
11 MemorIES3: a programmable, real-time hardware emulation tool for multiprocessor server design Ashwini Nanda, Kwok-Ken Mak, Krishnan Sugarvanam, Ramendra K. Sahoo, Vijayaraghavan Soundarararjan, T. Basil Smith November 2000 Proceedings of the ninth international conference on Architectural support for programming languages and operating systems, Volume 34, 28 Issue 5, 5						
	Full text available: pdf(724.53 KB) Additional Information: full citation, abstract, references, citings, index terms					
12	MemorIES: a programmable, real-time hardware emulation tool for multiprocessor server design Ashwini Nanda, Kwok-Ken Mak, Krishnan Sugavanam, Ramendra K. Sahoo, Vijayaraghavan Soundararajan, T. Basil Smith November 2000 ACM SIGPLAN Notices, Volume 35 Issue 11 Full text available: pdf(1.84 MB) Additional Information: full citation, abstract, references, index terms					
13	Silent stores for free Kevin M. Lepak, Mikko H. Lipasti December 2000 Proceedings of the 33rd annual ACM/IEEE international symposium on Microarchitecture Full text available: pdf(521.75 KB) ps(1.97 MB) Additional Information: full citation, references, citings, index terms					
	Publisher Site					
14	Register integration: a simple and efficient implementation of squash reuse Amir Roth, Gurindar S. Sohi					

Results (page 1): +dual +bus, +power +saving, +snoop, +high +power +bus, +low +powe... Page 2 of 3

Results 1 - 18 of 18

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2005 ACM, Inc.

Terms of Usage Privacy Policy Code of Ethics Contact Us

Useful downloads: Adobe Acrobat QuickTime Mindows Media Player Real Player